

What is claimed is:

Subcl/

1 1. A method comprising:
2 streaming at least two independent video sources
3 for display on a video display screen; and
4 causing said sources to be displayed at separate
5 regions of said display screen.

1 2. The method of claim 1 including forming said
2 sources into packets in a first device and transporting
3 said packets to a second device.

1 3. The method of claim 2 including depacketizing
2 said packets in said second device.

1 4. The method of claim 1 including transmitting said
2 video sources from a processor-based system to a display
3 device including said display screen.

1 5. The method of claim 4 including transmitting said
2 video sources over a wireless connection between said
3 processor-based system and said display device.

1 6. The method of claim 1, wherein said display
2 screen includes a pixel array and a memory array,
3 refreshing said memory array and said pixel array in the
4 same refresh cycle.

1 7. The method of claim 6 including displaying said
2 sources on a display that uses liquid crystal over
3 semiconductor technology.

1 8. The method of claim 1 including streaming video
2 sources for display on said display screen at different
3 frame rates.

1 9. The method of claim 1 wherein one of said video
2 sources includes television programming and the other of
3 said video sources includes graphical information.

1 10. The method of claim 1 including streaming a first
2 video source that includes television programming
3 information and a second video source that includes an
4 electronic programming guide information.

1 *Sub a17* 11. A system comprising:
2 a processor;
3 storage coupled to said processor;
4 a video controller coupled to said processor;
5 a packetization device coupled to said video
6 controller which independently packetizes at least two
7 ~~video streams.~~

1 12. The system of claim 11 including a modulation
2 device to modulate and transport said independently
3 packetized streams.

1 13. The system of claim 11 wherein each of said video
2 streams has a different frame rate and is packetized to be
3 de-packetized at the original frame rate in a display
4 device.

1 14. An article comprising a medium storing
2 instructions that cause a processor-based system to:
3 receive two independent video sources; and
4 packetize each of said video sources so that they
5 may be displayed in separate regions of a display screen.

1 15. The article of claim 14 further storing
2 instructions that cause a processor-based system to
3 transmit said video sources from said processor-based
4 system to a display device including a display screen.

1 16. The article of claim 15 further storing
2 instructions that cause the processor-based system to
3 transmit said video sources over a wireless connection
4 between said processor-based system and said display
5 device.

1 17. The article of claim 16 further storing
2 instructions that cause the processor-based system to
3 transmit said video sources for display on said display
4 screen at different frame rates.

1 18. A system comprising:
2 a semiconductor substrate;
3 a liquid crystal over semiconductor pixel array
4 formed in said substrate;
5 a memory coupled to said array, said memory also
6 formed in said substrate; and
7 a device for receiving a signal made up of a
8 plurality of independent video sources and driving each of
9 said video sources for display on a different portion of
10 said pixel array.

1 19. The system of claim 18 wherein said system
2 includes a device that de-packetizes said signal to form
3 independent video sources for display on said pixel array.

1 20. The system of claim 19 wherein said pixel array
2 includes a plurality of pixels including a memory cell.

1 21. The system of claim 20 wherein said memory cells
2 are static random access memory cells.

1 22. The system of claim 19 wherein said pixel array
2 is coupled to said memory by a digital to analog converter.

1 23. The system of claim 19 wherein said memory
2 includes a cell associated with each of a plurality of
3 pixels of said pixel array.

1 24. A system comprising:
2 an imaging device having a plurality of imaging
3 elements;
4 a memory that receives and stores at least two
5 independent video sources; and
6 a controller that drives said video sources onto
7 separate portions of said imaging device.

1 25. The system of claim 24 wherein said imaging
2 device is a thin film transistor imaging device.

1 26. The system of claim 24 wherein said imaging
2 device is a cathode ray tube.

1 27. The system of claim 24 wherein said imaging
2 device uses liquid crystal over semiconductor technology.

1 28. The system of claim 24 including a device to
2 receive packetized video information, de-packetize said

- 3 information and provide said de-packetized information to
4 said memory as independent video sources.